

Addendum No. 2 to RFP No. 0011204
February 10th, 2004

Except as specifically modified herein, this RFP remains unchanged. Proposers are reminded that receipt of this Addendum No. 2 must be acknowledged on Attachment A-1 or A-5 to the solicitation.

Question 1:

I have a question regarding the NVMM system. Other than statements requiring redundant power supplies and additional memory to meet EOL requirements, I do not see any requirements for single point failure immunity or redundancy for the user defined cPCI slots. Is there a single point failure immunity requirement? Does the system need to provide a redundant cPCI backplane and controller? Do you plan on using this system in a block redundant fashion?

Response 1:

With respect to the NVMM being a Subassembly: The cPCI backplane does not have to be redundant, nor is there a requirement that the NVMM be single point failure immune, other than as stated in §6.3.6.4 of Exhibit III "Non-Volatile Mass Memory Functional Requirements", dated January 27, 2004. Mass and volume consumed by power converters of appropriate size shall be counted against the overall allocation described in §10 of Exhibit III.

The NVMM is part of a block redundant system.

Question 2:

What is the anticipated total flight hardware quantity for the 1M SUROM device?

Response 2:

JPL can not anticipate the total flight hardware quantity at this time.

Question 3:

I have the following question regarding the SUROM development task contained in the referenced RFP:

Is it responsive, and acceptable for evaluation, to propose a device that has additional package pins/functions (36 vs. 32), if they are required to meet the device radiation hardness specifications?

Response 3:

The form factor described in §10 of Exhibit III "Start Up Read Only Memory (SUROM) Non-Volatile Memory Functional Requirements" dated January 27, 2004, is the recommended (not required) implementation. However, pin compatibility and other functional signal implementations as described in Exhibit III §6.2 should be maintained.